



Application No.: 10/630,799

Docket No.: SON-2791

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Kenichi Satori

Confirmation No.: 9760

Application No.: 10/630,799

Art Unit: 2824

Filed: July 31, 2003

Examiner: Andrew Q. Tran

For: NONVOLATILE SEMICONDUCTOR
MEMORY DEVICE WITH ERROR
DETECTION AND CORRECTION CIRCUIT
(AS AMENDED)

Allowed: March 18, 2004

AMENDMENT UNDER 37 C.F.R. 1.312
AFTER ALLOWANCE AND BEFORE PAYMENT OF THE ISSUE FEE

MS Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Subsequent to the Notice of Allowance of March 18, 2004, but before payment of the issue fee, please amend the above-identified application as follows:

Amendments to the Title begin on page 2 of this paper.

Amendments to the Specification begin on page 3 of this paper.

Amendments to the Claims begin on page 4 of this paper.

Remarks/Arguments begin on page 7 of this paper.

IN THE TITLE

Please amend the title as follows:

Nonvolatile Semiconductor Memory Device With Error Detection and Correction Circuit